



Alchip Technologies Flexible Business Model

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Flexible ASIC Biz Models



PD0

Partial spec-in, IP subsystems - - - - -

PD1

RTL/Netlist handoff - - - - -

PD2

GDS2 handoff - - - - -

PD 2.5









Chip handoff - - - - -

PD3

MP handoff - -





Handoff Points



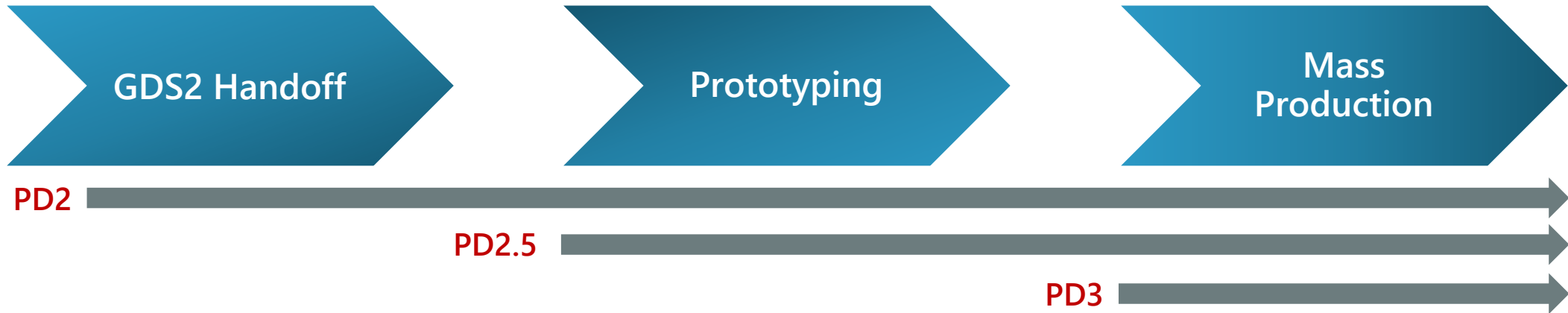
Point of Biz Handoff	PD0	PD1	PD2	PD2.5	PD3
Full Spec-in					
Partial Spec-in					
IP Subsystems					
RTL Handoff					
NL Handoff					
GDS2 Handoff					
Wafer Handoff					
Chip Handoff					
MP Handoff					

Post GDS2 Service Models



Point of Biz Handoff	PD2	PD2.5	PD3
GDS2 Handoff			
Wafer Handoff			
Chip Handoff			
MP Handoff			
Take-Out Operation (PE)	O	X	X
Package Design (APE)	O	X	X
Reliability Test (RA)	O	O/X	X
Test Program Development and Tooling Design (TE)	O	O/X	X
MP Correlation (PE/TE)	O	O	O

GDS2, Prototyping & Mass Production



- SI/PI/Thermal
- DFT/DFM
- Package/Substrate Design
- Test Program Development
- Pattern Conversion
- Probe Card Design
- Loadboard Design

- Mask/Wafer Fabrication
- Assembly
- Testing CP and FT
- ES Delivery
- ATE Troubleshooting
- Characterization
- Product Qualification

- Yield Improvement
- Test Time Reduction
- RMA Handling
- Failure Analysis
- Supply Chain Management
- Delivery Logistics
- Production Control
- Quality Assurance

Seamless GDS2 to Mass Production!

Summary

- Flexible HPC ASIC business model with a near-custom handoff points.
- Strong engineering and delivery capability specialize in post GDS2 tasks.
- Production experience across every TSMC advanced node... including 5nm.



alchip : " When Success is the Only Option! "



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