



Alchip Technologies 5nm Technology Platform

August 2020

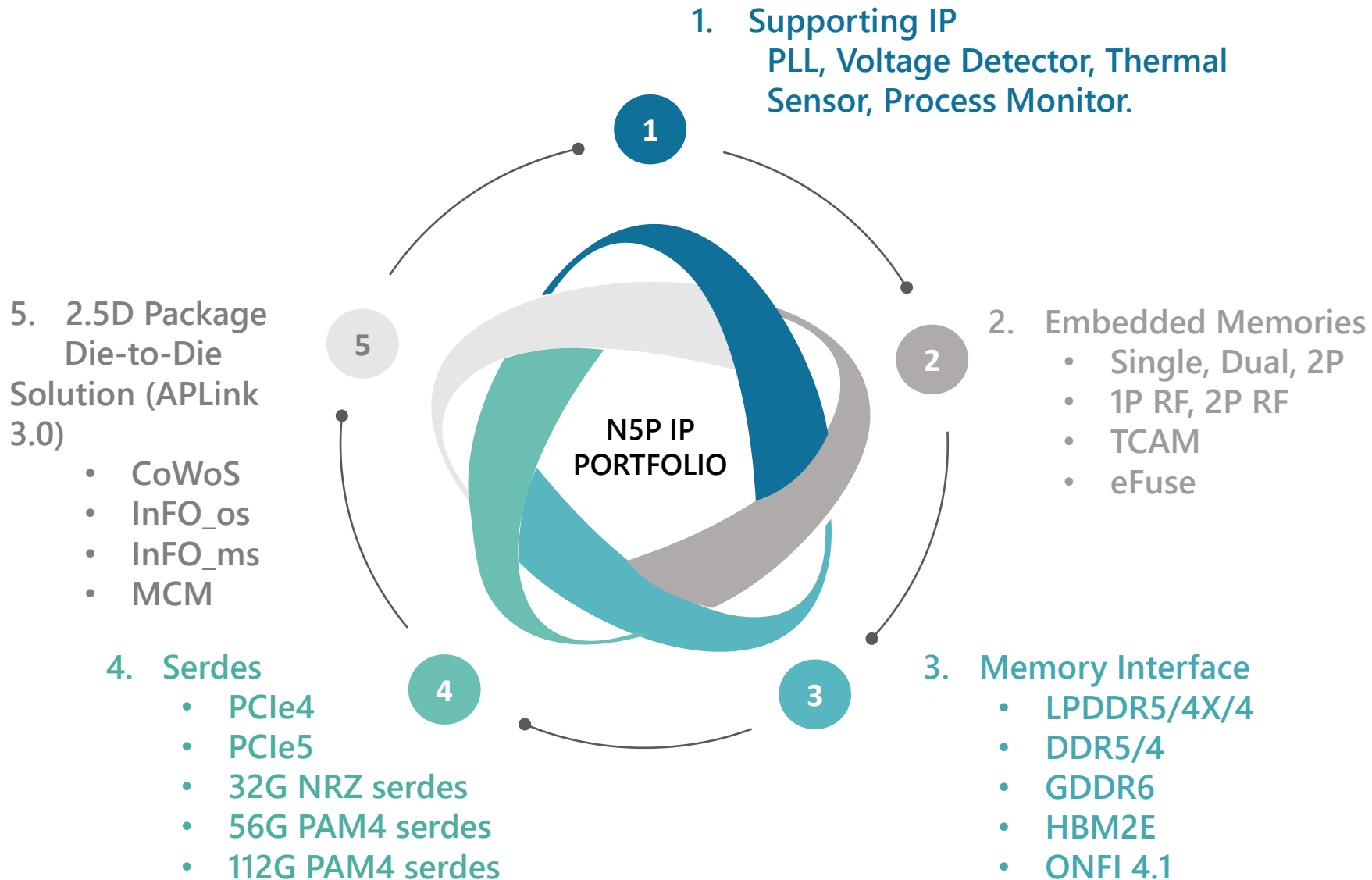
Silicon Heart of AI 

The "AI logo" consists of three concentric circles with dots on the perimeter, resembling a stylized atom or a network node.

N5P Physical Design Readiness

Schedule	2019		2020				2021	
Task	Q3	Q4	Q1	Q2	Q3	Q4	Q1	Q2
Design Technology and infrastructure ready	◆							
Design Flow ready			◆					
Design Methodology ready			◆					
Customer Engagement for testchip development				▨				
Testchip Specs finalized				◆				
Testchip T/O						◆		
Testchip Validation complete								◆

N5 IP Platform



- 5**: alchip
- 1**: Silicon CREATIONS
- 1**: moortec Global In-chip Monitoring
- 2 3 4**: SYNOPSYS®
- 3 4**: cādence
- 3 4**: Rambus
- 1**: proteanTecs
- 2**: tsmc
- 3**: M31
- 4**: Inphi Think fast.

Summary

- Alchip N5 design flow is production ready.
- Best-in-class 3rd party IP portfolio for HPC and AI designs.
- Advanced 2.5D CoWoS and InFO_os capability.
- IP available for 2.5D homogeneous and heterogeneous CoWoS and InFO_os integration.



Taipei · Hsinchu | Shin Yokohama | Silicon Valley | Shanghai · Wuxi · Hefei · Guangzhou · Jinan

Corporate Headquarters 9F, No.12, Wenhua St. Neihu Dist. Taipei City 11445, Taiwan

TEL: +886-2-2799-2318 | FAX: +886-2-2799-7389