

January
2026

Alchip Newsletter

Executive Corner

The year 2025 saw substantial advances for Alchip across our ecosystem. We introduced a 2nm design platform and advanced 3DIC readiness with a complex test chip.

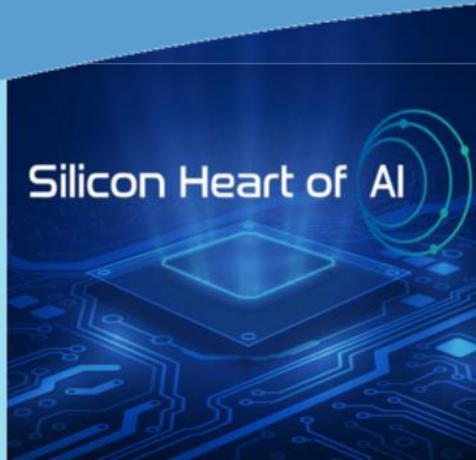
We also worked with several partners to advance CPU ASIC design, AI inference performance, and photonic co-packaged interconnect.

Details around some of these achievements are included in this newsletter.

In December, we reported that profits held steady on a quarter-by-quarter basis, with a 3.1% quarter-to-quarter increase in net income.

We look forward to delivering continued advances to our ecosystem in 2026.

Johnny Shen
Chairman of the Board,
President & CEO, Alchip



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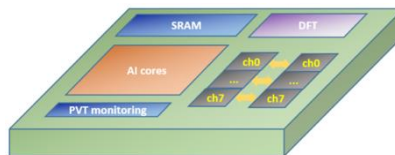
Technology News

Alchip Introduces 2nm Design Platform

Now available for High-Performance Computing and AI ASIC Designs

Alchip Technologies has received the first wafers from its groundbreaking 2nm Design Platform and is actively engaged with customers on high performance 2nm ASIC development.

[Read the press release here](#)

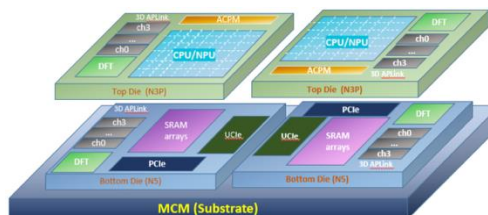


Alchip's 2nm test chip targets a gate all-around transistors structure

Alchip 3DIC Test Chip Tape Out Validates Ecosystem Readiness

Alchip Technologies validated its 3DIC ecosystem readiness with results from its 3DIC test chip tape out.

[Read the press release here](#)



Alchip's 3DIC test chip



Ecosystem News

Alchip Joins Arm Total Design Ecosystem

*Enhances collaboration on high-performance computing ASIC
CPU designs powered by Arm Neoverse Compute Subsystems*

Alchip Technologies announced today that it is joining Arm® Total Design, an ecosystem of industry leaders collaborating to accelerate and simplify the development of silicon based on Arm Neoverse® Compute Subsystems (CSS).

[Read the press
release here](#)

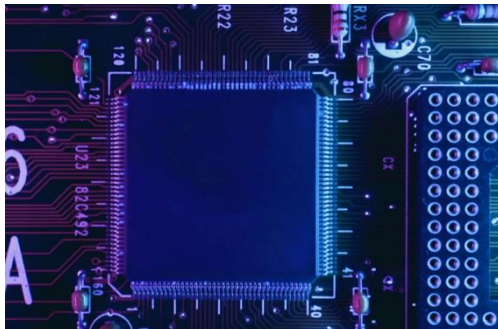
d-Matrix and Alchip Announce Collaboration on World's First 3D DRAM Solution to Supercharge AI Inference

*d-Matrix 3DIMC to Deliver 10x Faster Inference Than HBM4-Based Solutions;
Commercial Debut Planned With d-Matrix Raptor Inference Accelerator*

d-Matrix, the pioneer in generative AI inference for data centers, and Alchip are collaboratively developing the world's first 3D DRAM-based datacenter inference accelerator that will eliminate performance and cost bottlenecks constraining today's AI infrastructure.

[Read the press
release here](#)

Artilux Collaborates with Alchip to Create an Ultra-Low-Power Photonic Interconnect Platform to Accelerate the Deployment of Large-Scale AI Computing



mashdigi

[Read the article here](#)

Events

Alchip and Ayar Labs Unveil Co-Packaged Optics for AI Datacenter Scale-Up

Joint innovation enables ultra-high bandwidth, low latency, high radix, and energy efficiency for multi-rack AI clusters

At the 2025 TSMC North America Open Innovation Platform® (OIP) Ecosystem Forum, Alchip Technologies and Ayar Labs, a leader in co-packaged optics (CPO) for large-scale AI workloads, unveiled a CPO solution that is designed to efficiently extend scale-up network connectivity beyond traditional rack boundaries.

[Read the press release here](#)



Alchip Technologies and Ayar Labs scale model of their joint co-packaged optics solution